



TITLE: ON-CHIP SUBSTRATE REGULATOR TEST MODE
INVENTOR NAME: Gary R. Gilliam
SERIAL NO.: 09/935,232

1/3

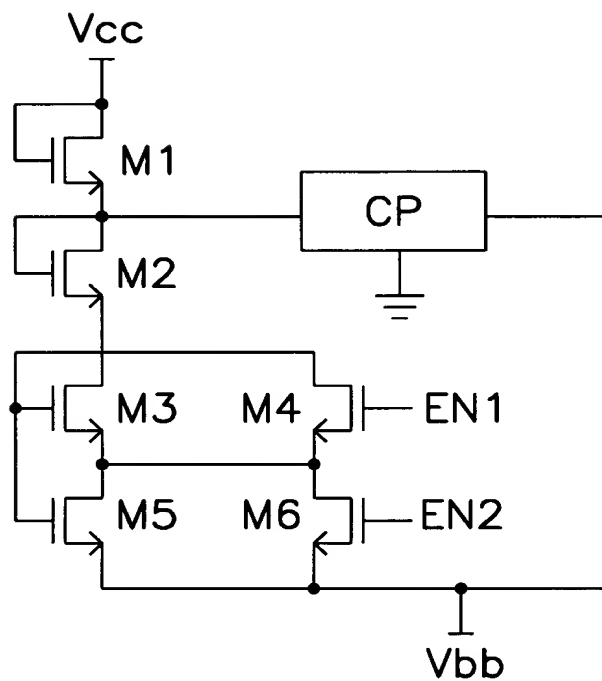


FIG. 1



TITLE: ON-CHIP SUBSTRATE REGULATOR TEST MODE
INVENTOR NAME: Gary R. Gilliam
SERIAL NO.: 09/935,232

2/3

$$\begin{aligned}V_{GS} &= 1 - V_S \\V_{DS} &= -1.0 - V_S \\V_{GS} - V_{DS} &= 2 \\V_{GS} &= V_{DS} + 2\end{aligned}$$

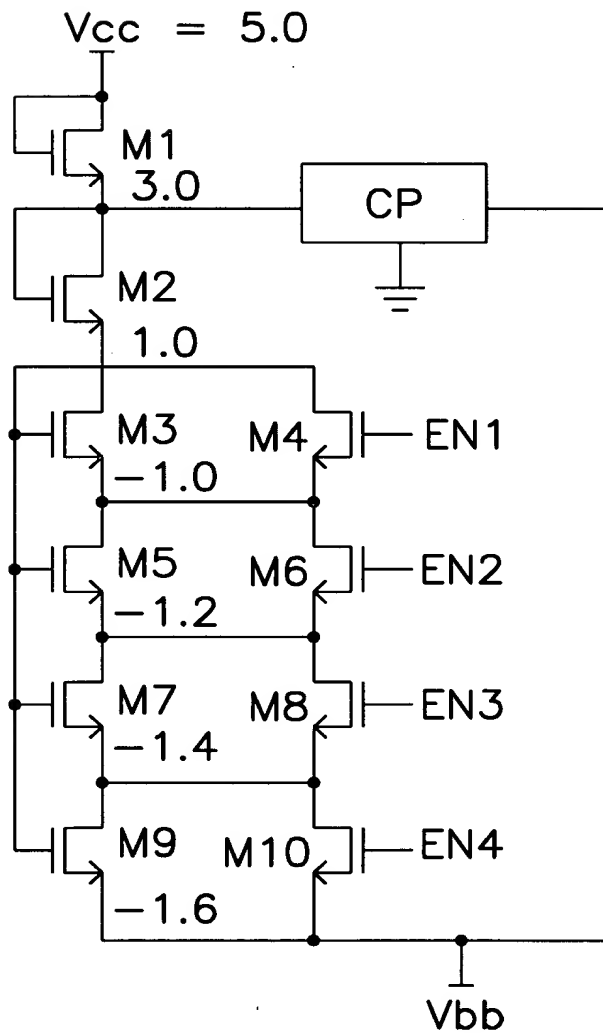


FIG. 2



TITLE: ON-CHIP SUBSTRATE REGULATOR TEST MODE
INVENTOR NAME: Gary R. Gilliam
SERIAL NO.: 09/935,232

3/3

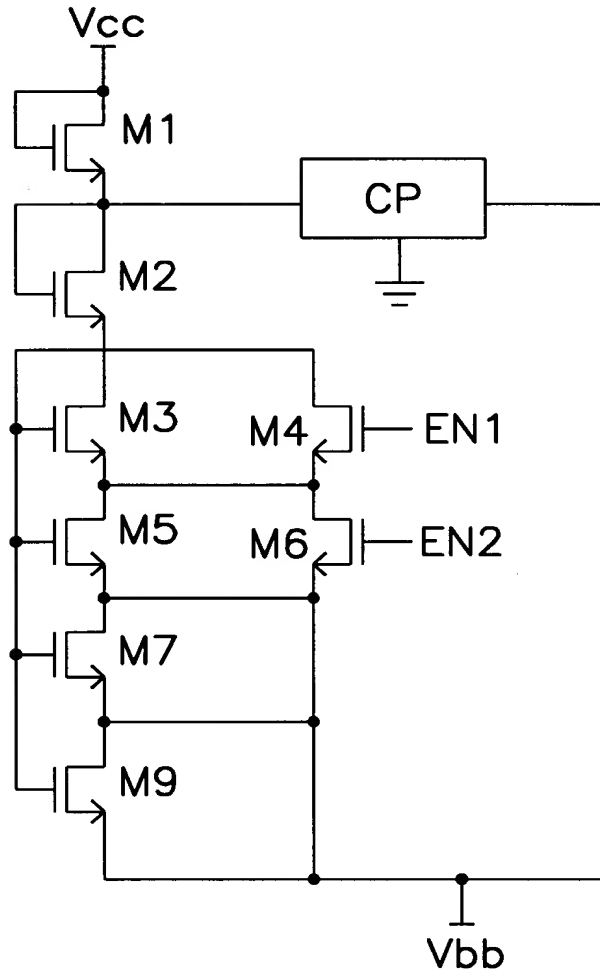


FIG. 3